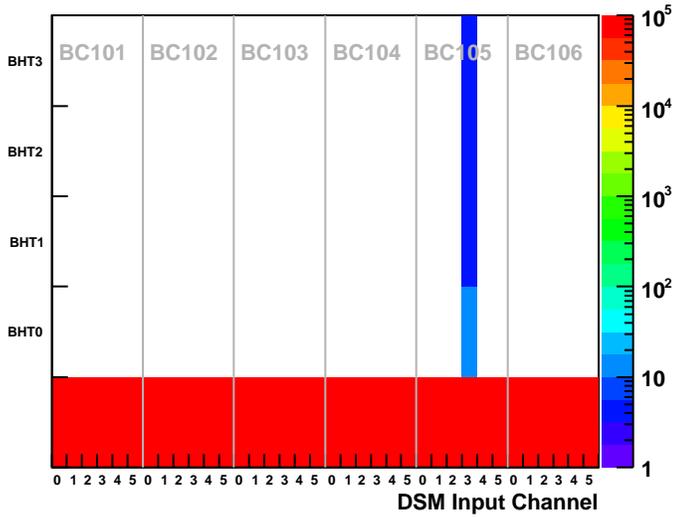


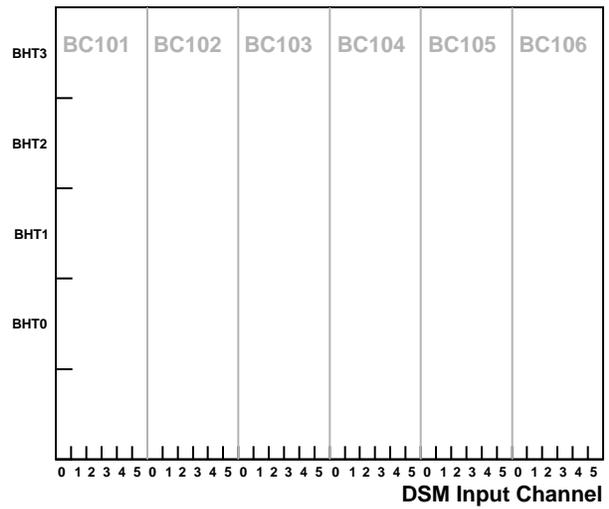
Barrel EMC L1 Input - High Tower Bits

Entries 3600000



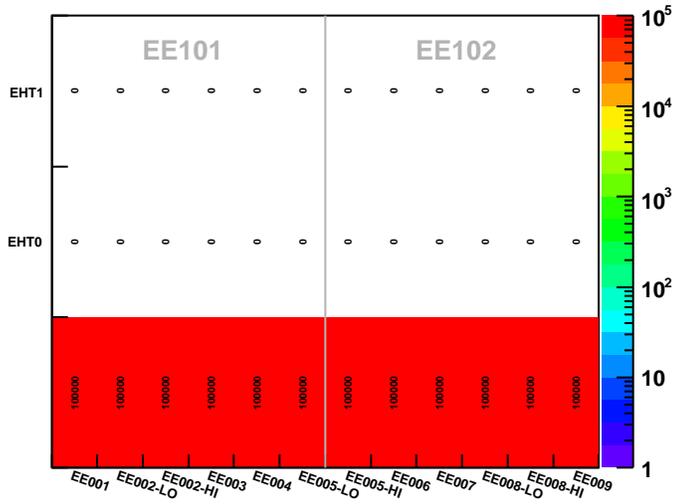
Barrel EMC L1 Input - High Tower Bits Errors

Entries 0



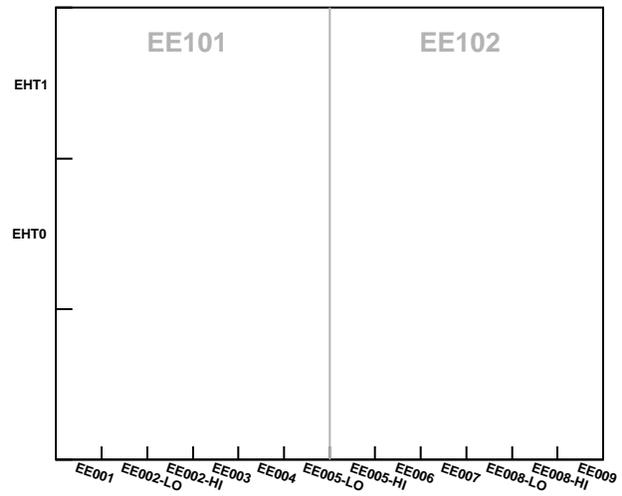
Endcap EMC L1 Input - High Tower Bits

Entries 1200000



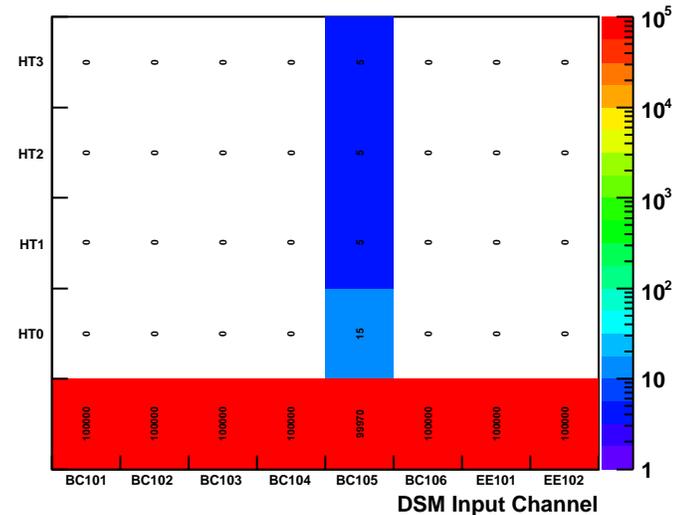
Endcap EMC L1 Input - High Tower Bits Errors

Entries 0



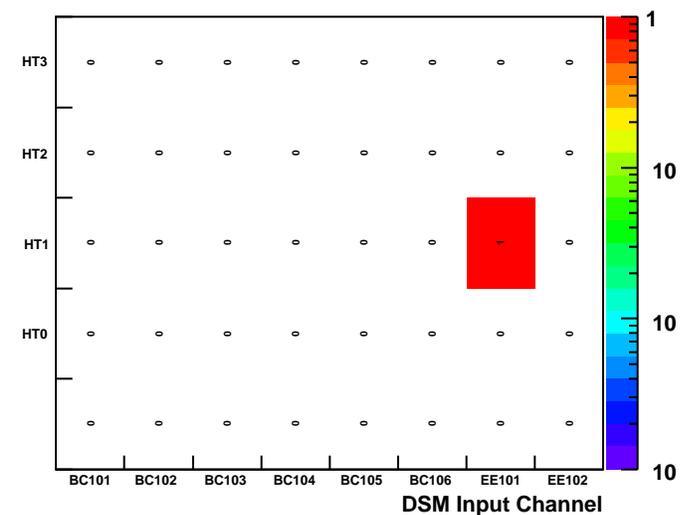
EMC L2 Input - High Tower Bits

Entries 800000



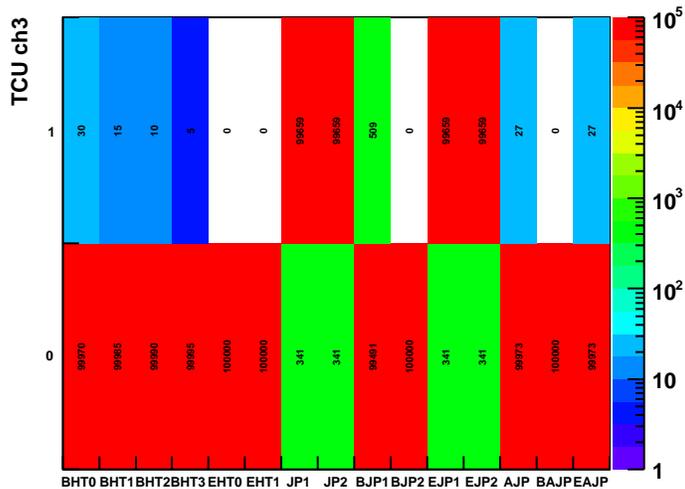
EMC L2 Input - High Tower Bits Errors

Entries 1



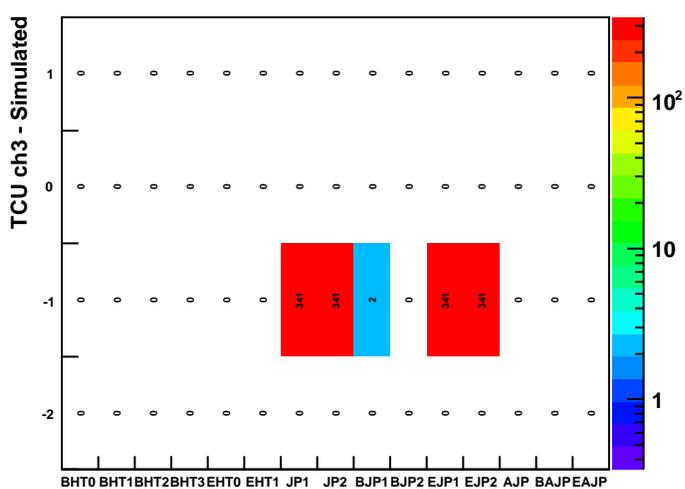
TCU ch3 Input (EM201)

Entries 1500000



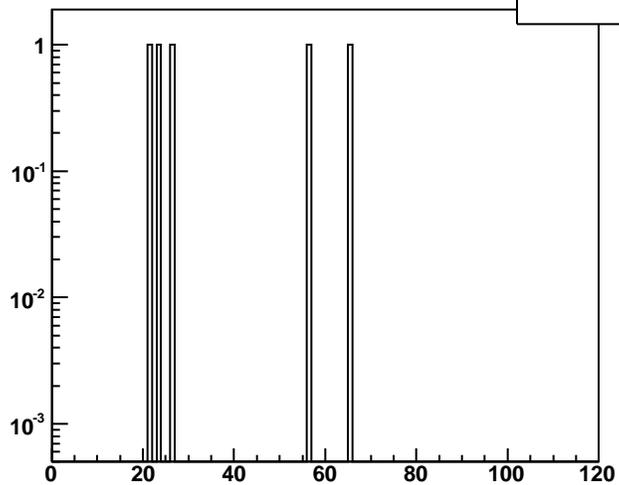
TCU ch3 Input (EM201)

Entries 1366



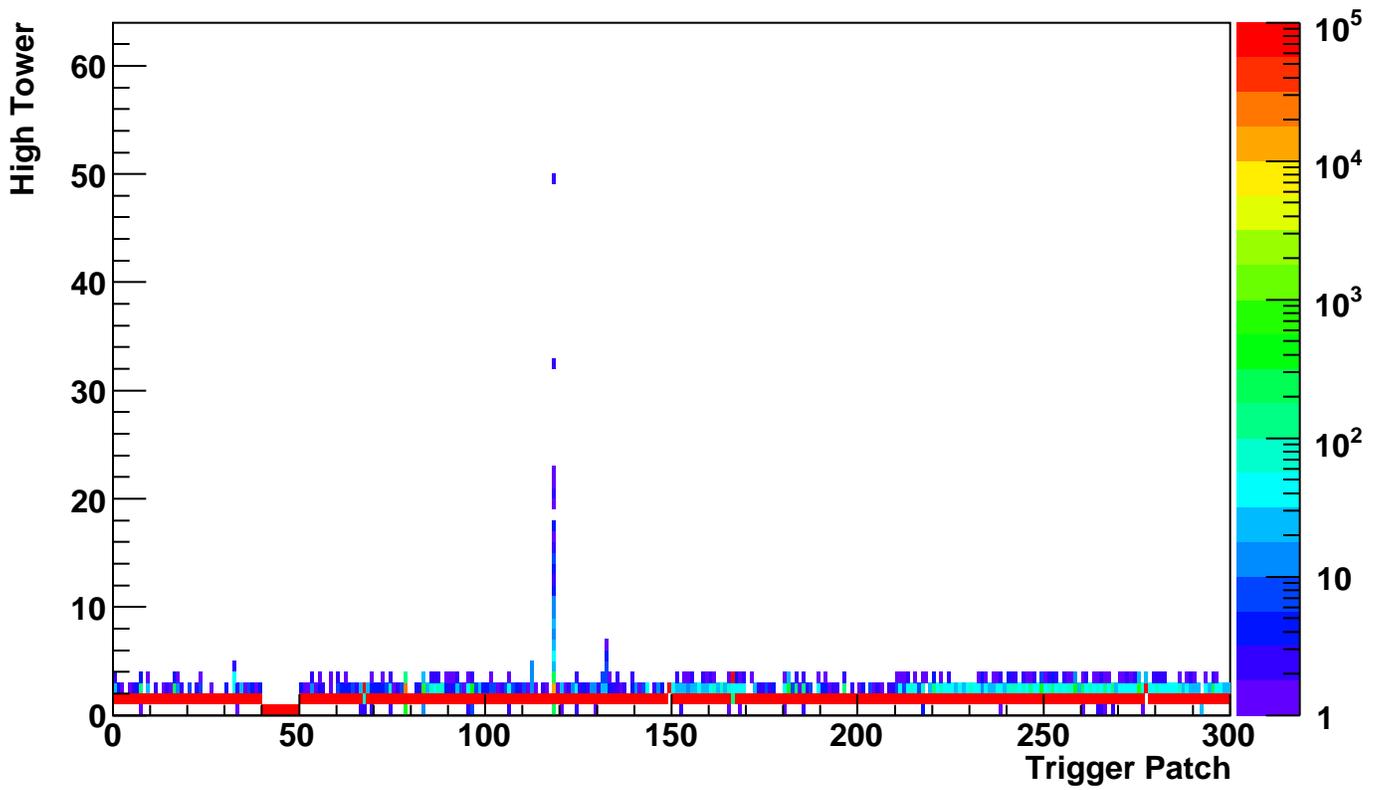
BunchId7Bit (BHT3)

Entries 5



Barrel EMC L0 Input - High Tower

Entries 3e+07



Endcap EMC L0 Input - High Tower

Entries 9000000

